

IN THE SPECIFICATION:

Please amend the paragraph beginning at page 15, line 8, as follows.

It should be noted that down spreading from a frequency divider value of 50 to a frequency divider value of 49 can be too much down spreading for some applications. For example, at 500 MHz using a reference clock of 10 megahertz (MHz), a decrease in the frequency divider value from 50 to 49 would be two percent down spreading. Some applications might require a lower amount of spreading. This type of application could then use only the down spreading determined using references 360 and 361, for example, which use approximations 310 and 320 and an equivalent set of approximations to spread from f_{low} of 362 back to f_{nom} .

Please amend the paragraph beginning at page 19, line 2, as follows.

FIG. 7 is an illustration of modifications performed by a down-to-up profile converter 285 in order to convert entries in a memory 200 from down slewing to up slewing. The example of FIG. 7 uses a clock generation circuit 200 where there are eight possible phases from a VCO. Table 700 is a portion of memory 200, and table 710 is the portion 700 after the down-to-up profile converter 285 has converted the portion 700 for use in spreading up. In an ~~and~~ exemplary embodiment, the down-to-up profile converter 285 ignores the frequency divider values in table 700. Instead, the down-to-up profile converter 285 maps the phases in table 700 to equivalent phases in table 710. For instance, phase 1 of table 700 is unchanged (see entry 717-1), phase eight is changed to phase two (see entry 717-2) and phase seven is changed to phase three (see entry 717-3). Entries 717-4 through 717-8 can be similarly determined. When the down-to-up profile converter 285 reaches phase eight (in entry 717-8), the down-to-up profile converter 285 is programmed so that the next phase in entry 717-9 is phase one, but the frequency divider value is temporarily modified by one (from $M=50$ to $M=51$) in order to maintain a period of the feedback signal 251. In entry 717-10, the down-to-up profile converter 285 modifies the frequency divider value back to 50 from 51 in order to maintain a period of the feedback signal 251.